



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re the Application of:

VASUDEVAN SRINIVASAN, ET AL.

Application No.: 10/750,395

Filed: December 30, 2003

For: **Method and Apparatus for Determining
Processor Utilization**

Art Group: 2192

Examiner: Chuck O. Kendall

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted before the mailing of a final Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

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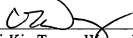
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Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: April 18, 2007


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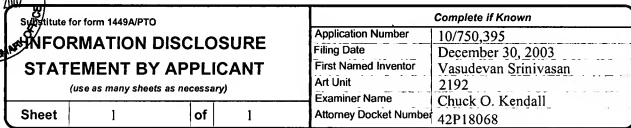
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Examiner Signature	/Chuck Kendall/	Date Considered	12/05/2008
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